

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|--|---|------------------|---------|---------------------|
| L1 | 1886 | (eletro\$magnetic "EM") near10 analy\$6 | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/08/15 13:11 |
| L2 | 127 | L1 and (partition\$3 divid\$4 decomposit\$4 segment) with (conductor chip circuit PCB plane) | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/08/15 13:12 |
| L3 | 70 | L2 and interconnect\$3 | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/08/15 13:13 |
| L5 | 44 | L3 and (addition add\$3 extra) near10 (port input output input\$output IO terminal pin) | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/08/15 13:14 |
| L6 | 1 | L5 and (716/1-5,18.ccls. 703/13-22.ccls.) | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/08/15 13:15 |
| L7 | 17 | L2 and (716/1-5,18.ccls. 703/13-22.ccls.) | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/08/15 13:16 |
| L8 | 131 | L1 same (partition\$3 divid\$4 decomposit\$4 segment) | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/08/15 13:20 |
| L9 | 6 | L8 and (716/1-5,18.ccls. 703/13-22.ccls.) | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/08/15 13:21 |
| L10 | 151 | (eletro\$magnetic electro \$migration "EM") near10 analy\$6 same (partition\$3 divid\$4 decomposit\$4 segment) | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/08/15 13:22 |

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|-----|-----|--|---|----|----|---------------------|
| L11 | 21 | L10 and (716/1-5,18.ccls. 703/13-22.ccls.) | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/08/15 13:22 |
| L12 | 106 | (eletro\$magnetic "EM") same analy\$6 with segment \$5 | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/08/15 13:25 |
| L13 | 1 | L12 and (716/1-5,18.ccls. 703/13-22.ccls.) | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/08/15 13:25 |
| L14 | 42 | L12 and (partition\$3 divid\$4 decomposit\$4 segment) with (conductor chip circuit PCB plane) | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/08/15 13:25 |
| L15 | 2 | L14 and @ad<="20051011" | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/08/15 13:26 |

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